

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Norio HASEGAWA et al.

Serial No.: Not yet assigned
Continuation Application of Serial No. 09/707,833

Filed: Even date herewith

For: **METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICES**

Group: 1756 (expected)

Examiner: Kripa Sagar (expected)

CLAIM FOR PRIORITY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 4, 2004

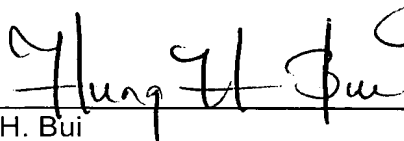
Sir:

Pursuant to 35 U.S.C. §119 and 37 CFR 1.55, the applicants hereby claim the right of priority based on the following foreign application filed in Japan:

Application No. 2000-039706 filed on February 17, 2000.

A certified copy of this foreign application was filed on November 8, 2000, in application Serial No. 09/707,833, the parent application of the present continuation application.

Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS, LLP



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